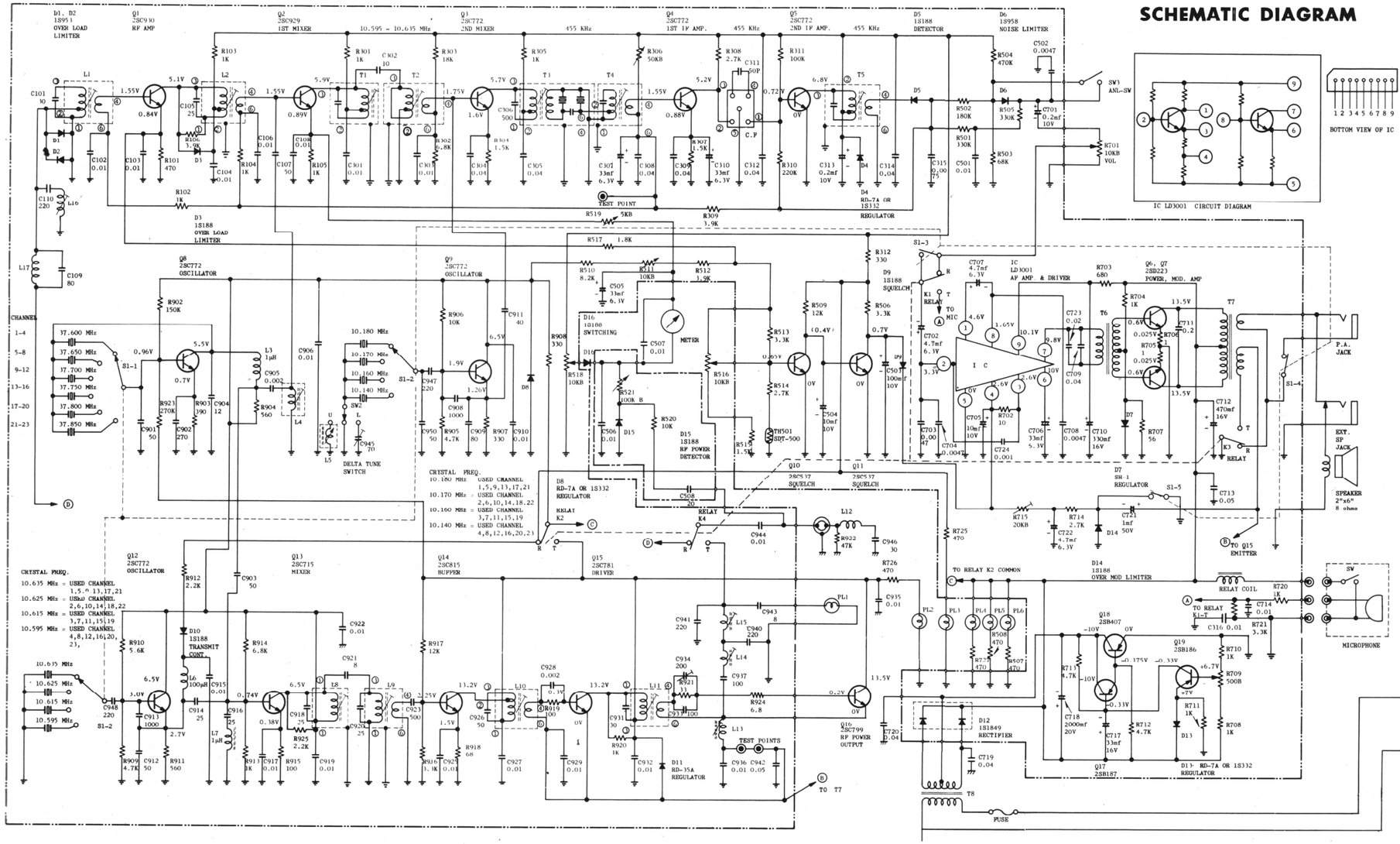
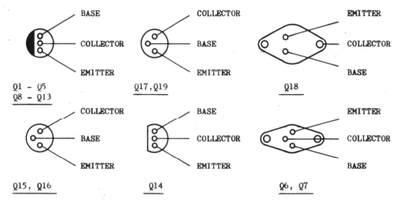
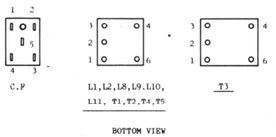
SCHEMATIC DIAGRAM



IC LD3001 CIRCUIT DIAGRAM





NOTE:

1. All resistance values are in ohms, K=1,000 ohms.
2. All capacitance values less than 1.0 are in MFD and greater than 1.0 are in PFD unless otherwise specified.
3. All voltages are measured from the common negative ground (-) to respective terminals of transistors and integrated circuit (IC) with V.T.V.M. under no signal conditions.
4. PL1 is modulation indicator.
   PL2 is XMIT indicator.
   PL3 is receive indicator.
   PL4 and PL5 are meter illuminations.
   PL6 is channel selector illumination.

COURIER MODEL "CITATION"
ISSUE C