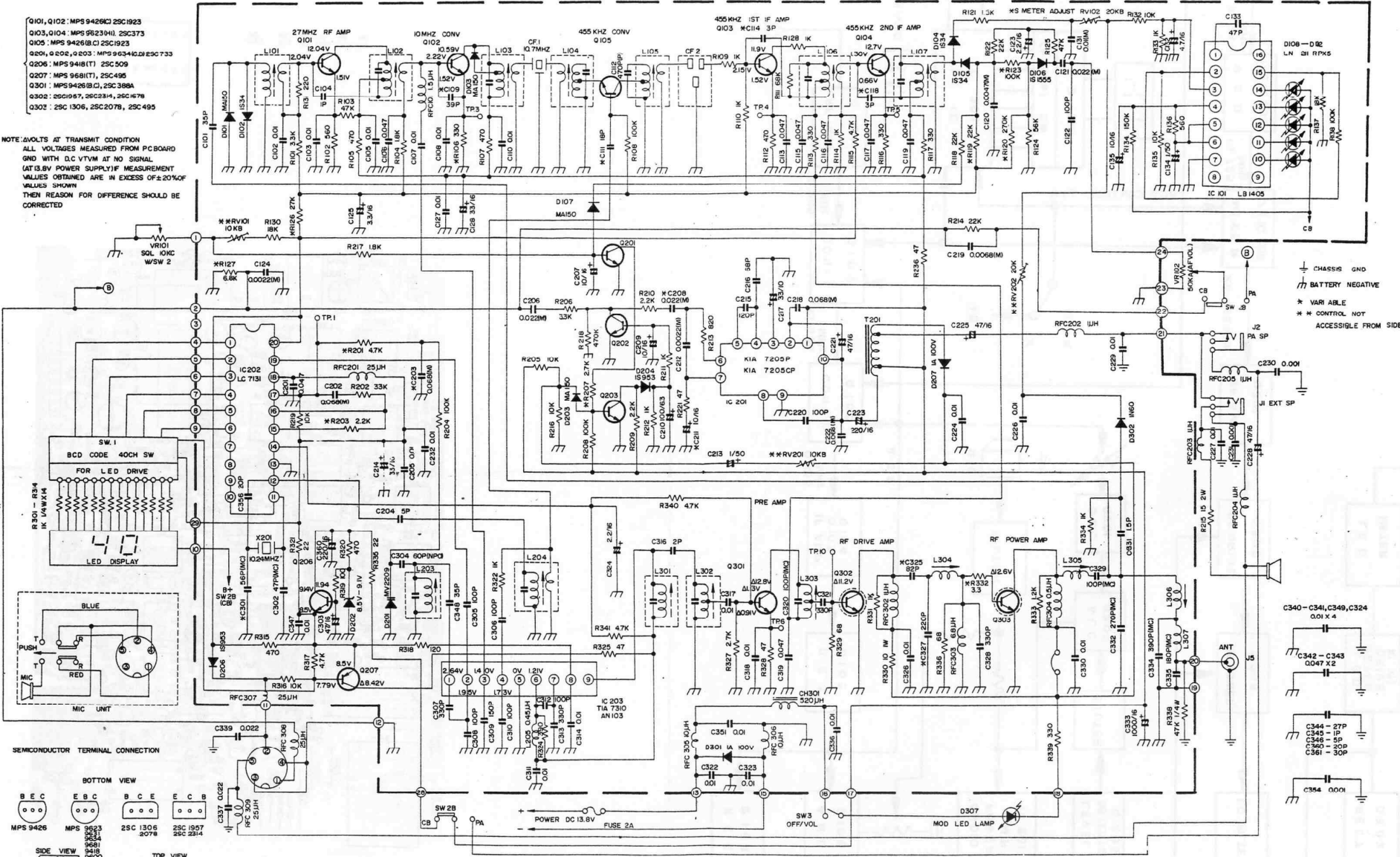


- Q101, Q102: MPS 9426(C) 25C1923
- Q103, Q104: MPS 9623(H) 25C373
- Q105: MPS 9426(B) C1 25C1923
- Q204, Q202, Q203: MPS 9634(G) 25C373
- Q206: MPS 9418(T) 25C509
- Q207: MPS 9618(T), 25C495
- Q301: MPS 9426(B) C1, 25C388A
- Q302: 25D1947, 25C2814, 25C1678
- Q303: 25C1306, 25C2078, 25C495

NOTE: VOLTS AT TRANSMIT CONDITION  
 ALL VOLTAGES MEASURED FROM PC BOARD  
 GND WITH D.C. VTVM AT NO SIGNAL  
 (AT 13.6V POWER SUPPLY) IF MEASUREMENT  
 VALUES OBTAINED ARE IN EXCESS OF ±20% OF  
 VALUES SHOWN THEN REASON FOR DIFFERENCE SHOULD BE  
 CORRECTED

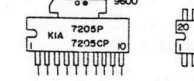


SEMICONDUCTOR TERMINAL CONNECTION

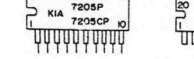
BOTTOM VIEW



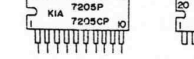
SIDE VIEW



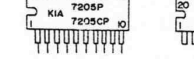
TOP VIEW



SIDE VIEW



SIDE VIEW



# JAWS MARK 2 SCHEMATIC DIAGRAM

- ⊥ CHASSIS GND
- ⊖ BATTERY NEGATIVE
- \* VARI ABLE
- \* \* CONTROL NOT ACCESSIBLE FROM SIDE

- C340 - C341, C349, C324 0.01 X 4
- C342 - C343 0.047 X 2
- C344 0.01
- C345 1.1
- C346 1.1
- C347 1.1
- C348 1.1
- C349 1.1
- C350 1.1
- C351 1.1
- C352 1.1
- C353 1.1
- C354 0.001