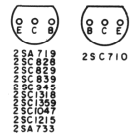
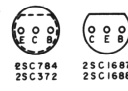


NOTE
 Δ VOLTS AT TRANSMIT CONDITION
 ALL VOLTAGES MEASURED FROM COMMON
 NEGATIVE CHASSIS GND WITH D.C. VTVM
 AT NO SIGNAL (AT 18.0V POWER SUPPLY)
 IF MEASUREMENTS OBTAINED ARE IN
 EXCESS OF ±20% OF VALUES SHOWN
 THEN REASON FOR DIFFERENCE SHOULD
 BE CORRECTED
 ⊕ CHASSIS GND ⊖ BATTERY NEGATIVE
 * VARIABLE

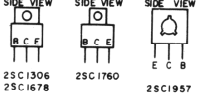
SEMICONDUCTOR



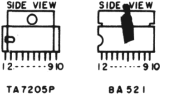
TERMINAL



CONNECTION (BOTTOM VIEW UNLESS SIDE VIEW)

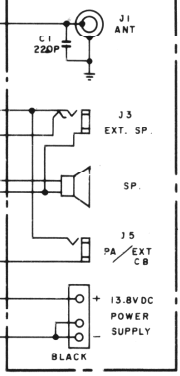


OTHERWISE NOTED) SIDE VIEW



E : EMITTER
 C : COLLECTOR
 B : BASE

REAR PANEL



FRONT PANEL

