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**Sharp CB-2260 Service Manual**

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This model CB-2260 is almost the same as the model CB-2460 except for the channel display circuit and some external parts. As to the descriptions about "Trouble Shooting Guide", "Cautions on Handling MOS IC" and "Equivalent Circuit of IC", refer to the Service Manual for the model CB-2460.

## 40 Channel C.B. Transceiver MODEL CB-2260

**"WARNING"**  
It is unlawful for the user to make any replacement or substitution of parts, adjustments or to service the transmitter by any one other than a person holding a commercial 1st or 2nd class radio operator's license. Any change in the circuitry that would change or violate the technical regulations or type acceptance is prohibited.

### SPECIFICATIONS

#### Transmitter section

RF power output ..... 4W (maximum)  
 Frequency range ..... 27MHz Citizens Band  
 Channels ..... 40 chs. P.L.L. (Phase Locked Loop) circuit Synthesizer  
 Type of crystal ..... HC-18U  
     Tolerance  
     10.240MHz . . . . . ± 0.004%  
     Others . . . . . ± 0.003%  
 Transmitter modulation ... 100% (maximum)  
 Modulation limiter ..... Yields high average modulation at average voice levels  
 Antenna matching ..... Nominal 50 ohms  
 Carrier deviation ..... Not greater than ± 800Hz nominal on (exceeds F.C.C., D.O.C., etc. requirements)  
 Harmonic suppression ..... Exceeds 60dB

#### Receiver section

Audio power output ..... 3.5 Watts maximum power output  
 Sensitivity ..... 0.7 $\mu$ V/m for 10dB S + N/N ratio at 30% at 1000Hz modulation  
 Channels ..... 40 chs. P.L.L. (Phase Locked Loop) circuit Synthesizer  
 Type of crystal ..... HC-18U  
     Tolerance  
     11.730MHz . . . . . ± 0.003%  
 Selectivity ..... 6dB down at ±3kHz;  
     50dB down at ± 10kHz.  
 Intermediate frequency.... 1st-IF: 11.275MHz,

2nd-IF: 455kHz  
 Circuit type..... Dual conversion superheterodyne: P.L.L. circuit frequency synthesizer provides 40 transmit and receive channels. Delta tuning of ±1.0kHz on each channels plus ceramic filter.  
 Auxiliary circuits ..... Automatic noise limiter (ANL), Variable squelch, Public Address System (P.A.)

#### General

Power source ..... DC 12.0V Nominal negative or positive ground  
 Antenna ..... 50 ohm external antenna for car or base operation  
 Speaker ..... 3-1/8" P.D.S. 8-ohm Imp.  
 Microphone ..... Press-to-talk dynamic microphone (500 ohm)  
 Accessories ..... Microphone hanger  
     Mobile mounting bracket  
     Mounting screws  
     Microphone with plug and cord.  
     Power supply cord with fuse holder and socket.  
     Spare fuse (2.3A)  
 Dimensions ..... 2-1/4"(H) x 5-3/4"(W) x 7-7/8"(D)  
 Weight ..... 4 lbs. with microphone  
 Cabinet..... Metal body with plastic front

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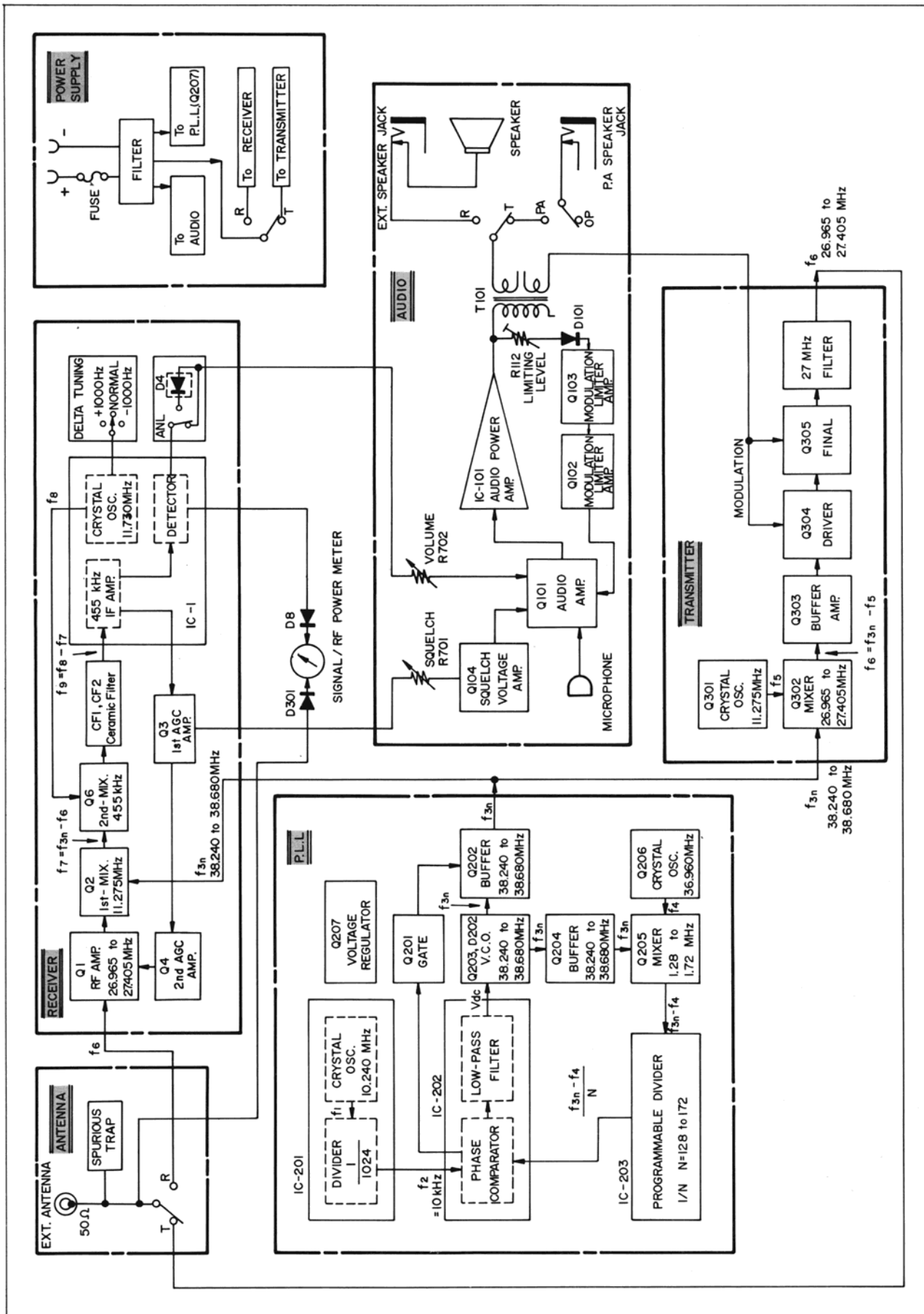


Figure 1 BLOCK DIAGRAM

## GENERAL DESCRIPTION (Refer to Figure 1)

### RECEIVER SECTION

An input signal sent from the antenna is applied to the 1st-mixer of transistor Q2 via the RF amplifier of transistor Q1, and an oscillator signal sent from transistor Q202 is also applied to the 1st-mixer of transistor Q2. In this stage the above-mentioned input signal is converted to 1st-IF signal of 11.275 MHz.

The 1st-IF signal (11.275 MHz) is applied to the 2nd-mixer of transistor Q6 via the transformers T3 and T4 to be converted to 2nd-IF signal of 455 kHz. The 2nd-IF signal is applied to the pin ⑦ of IC-1 via the transformer T5 and ceramic filters CF1 and CF2. (The 2nd-IF signal is amplified between the pin ⑦ and pin ⑧ of IC-1 and it is also detected between the pin ⑫ and pin ⑪).

The detected output signal developed at the pin ⑪ of IC-1 is further applied to IC-101 consisting of drive circuit and power amplifier via audio amplifier of transistor Q101.

### TRANSMITTER SECTION

The audio signal from the microphone is applied through the audio amplifier Q101 and the output of IC-101 to the final stage Q305 and the drive stage Q304.

The carrier signal synthesized in the P.L.L. circuit, the oscillator Q301 and mixer Q302 is supplied to the final amplifier Q305 where it is modulated with the audio signal and applied to the antenna for transmission.

## A DESCRIPTION OF PHASE-LOCKED-LOOP (P.L.L.) CIRCUIT (Figure 2)

### 1) What is P.L.L. ?

P.L.L. is abbreviation of Phase-Locked-Loop which synchronizes with frequency and phase of the stable standard input (crystal oscillation) given from the outside, namely working not only as automatic frequency control but also as automatic phase control.

The P.L.L. is now used to realize a synthesizer. Consisting of two crystals, the synthesizer serves as an oscillator to oscillate step by step (10 kHz) in the range of 38.240 MHz ~ 38.680 MHz.

Therefore, this synthesizer can be said to be on the same level in the connection with the accuracy and stability of oscillation as the crystal oscillator.

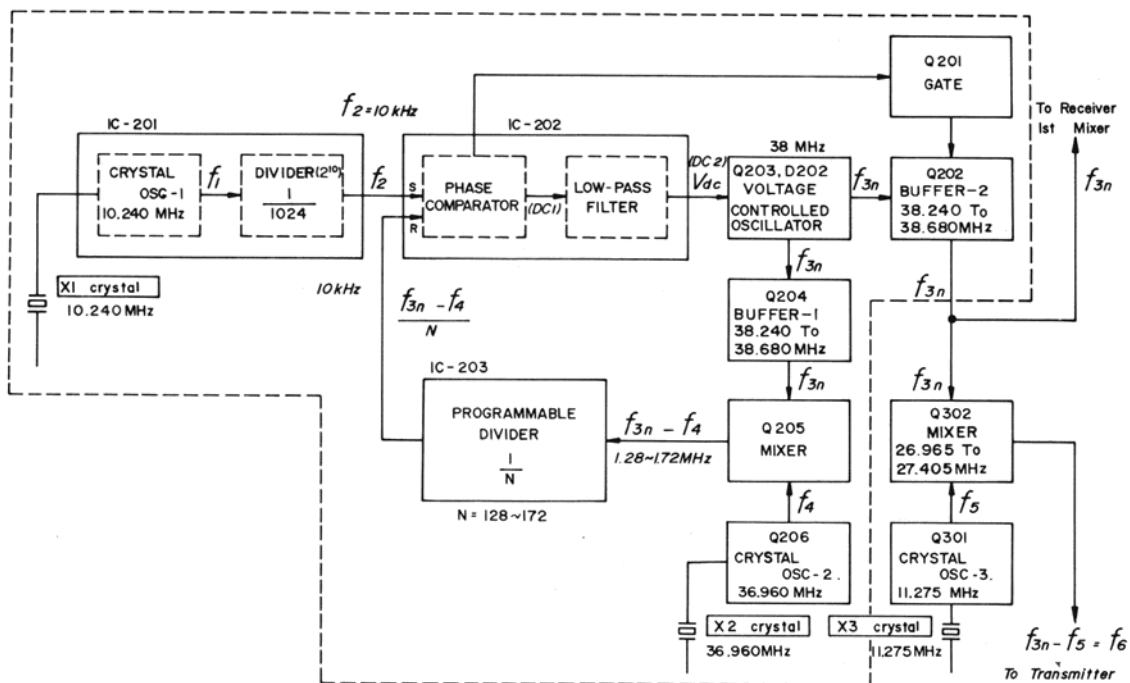


Figure 2 P.L.L. CIRCUIT FREQUENCY SYNTHESIZER

### 2) Frequency Synthesizer

The frequencies for both transmitter and receiver are synthesized by three crystal controlled oscillators and the Phase-Locked-Loop (or P.L.L.) consisting of six basic building blocks: the phase detector (phase comparator) IC-202, the low-pass filter

IC-202, the voltage controlled oscillator (or V.C.O.) Q203, the buffer amplifier Q204, the mixer Q205 and the programmable divider IC-203 as shown in Figure 2.

3) **Frequency Determining** (Refer to Figure 2)

- 1 - A crystal oscillator consisting of a crystal X 1 (10.240MHz) and IC-201 generates a basic frequency  $f_1$  (=10.240MHz) and it is divided down through a fixed divided network (a portion of IC-201) to 10kHz frequency ( $f_2$ ). The frequency 10kHz is applied to the input of a phase comparator IC-202.
- 2 - A second oscillator Q203 is a voltage controlled oscillator (V.C.O.) and its frequency ( $f_{3n}$ ) is determined with a DC voltage ( $V_{dc}$ ) from IC-202. The output frequency ( $f_{3n}$ ) is applied to a mixer (Q205) through a buffer amplifier (Q204).
- 3 - A third oscillator consisting of a crystal X 2 (36.960MHz) and a transistor Q206 generates a frequency  $f_4$  (=36.960 MHz) and feed its frequency to the mixer Q205 also.
- 4 - Although the mixer Q205 produces two frequency signals  $f_{3n} + f_4$  and  $f_{3n} - f_4$ , the frequency  $f_{3n} - f_4$  only is applied to a programmable divider IC-203 through a low-pass filter consisting of a coil L201 and capacitors C214 and C216.
- 5 - The programmable divider IC-203 divides the frequency  $f_{3n} - f_4$  by the frequency divider number N, which is programmable by the switch position of the channel selector connected to the terminal pins 1 to 6 and 8 of IC-203. The assigned number is shown in Table 1. The output frequency  $(f_{3n} - f_4) / N$  is close to 10kHz and is fed back to the phase comparator of IC-202.
- 6 - The phase comparator of IC-202 compares the frequency  $f_2$  (= 10kHz) and the other frequency  $(f_{3n} - f_4) / N$  from the programmable divider and generates a D.C. voltage  $V_{dc}$  proportional to the phase differences of both frequencies. The voltage  $V_{dc}$  goes back to the V.C.O. Q203 through a low-pass filter.
- 7 - In this method, a closed-loop frequency-feedback system, which is so called P.L.L., is formed and the frequency  $f_{3n}$  of V.C.O. Q203 is locked.
- 8 - When the P.L.L. is in lock, two frequencies to phase comparator input are the same and therefore the frequency  $f_{3n}$  is determined as follows:

$$f_{3n} = Nf_2 + f_4$$

Where  $f_2 = 10\text{kHz}$

$$f_4 = 36.960\text{MHz}$$

$$N = 128 \text{ to } 172 \dots \dots \text{ Determined by channel selector as shown in Table 1.}$$

For example, the frequency  $f_{3n}$  of channel 1 is calculated as follows:

$$f_{3n} = 128 \times 0.01 + 36.960 \text{ (MHz)}$$

$$= 38.240 \text{ (MHz)}$$

Where "N = 128" is assigned for channel 1 by channel selector.

This frequency  $f_{3n}$  is applied to the first mixer of receiver and a mixer Q302 of transmitter through a buffer amplifier Q202 and a filter block T202.

- 9 - DC voltage condition may vary according to the frequencies as tabulated below.

Lock frequency condition:  $f_2 = \frac{f_{3n} - f_4}{N}$

$f_{3n}$	$f_{3n} - f_4$	$\frac{f_{3n} - f_4}{N}$	$f_2 \cdot \frac{f_{3n} - f_4}{N}$	Voltage DC <sub>1</sub>	Voltage DC <sub>2</sub>	$f_{3n}$	Final frequency
Rise (↑)	Rise (↑)	Rise (↑)	<	Rise (↑)	Drop (↓)	Drop (↓)	Lock
Drop (↓)	Drop (↓)	Drop (↓)	>	Drop (↓)	Rise (↑)	Rise (↑)	Lock
Lock	Still	Still	=	Still	Still	Still	Lock

- 10 - **The Transmitter Frequency**

The transmitter frequency  $f_6$  is determined by mixing  $f_{3n}$  and  $f_5$  signal which is generated by a crystal oscillator consisting of Q301 and crystal X 3. (= 11.275 MHz) and

$$f_6 = f_{3n} - f_5$$

$$= (Nf_2 + f_4) - f_5$$

Where  $f_5 = 11.275\text{MHz}$

Consequently, the transmitter frequency  $f_6$  is all crystal controlled. Table 1 shows the synthesized frequencies for each channel.

- 11 - Gate (transistor Q201) shown in the block diagram works to detect a lock condition of the P.L.L. circuit and to take out an output only at the lock condition, controlling the buffer-2 (transistor Q202) amplifier --- it is thus prevented that an unstable signal is emitted when the P.L.L. circuit can not be locked for some reason.

**FREQUENCY OF SYNTHESIS CHART**

TUNING FREQUENCY (MHz)	CHANNEL	N	$f_1$ (MHz)	$f_2$ (kHz)	$f_{3n}$ (MHz)	$f_4$ (MHz)	$f_5$ (MHz)	$f_{3n}-f_4$ (MHz)	$f_{3n}-f_5$ (MHz)
26.965	(1)	128	10.240	10	38.240	36.960	11.275	1.280	26.965
26.975	(2)	129	10.240	10	38.250	36.960	11.275	1.290	26.975
26.985	(3)	130	10.240	10	38.260	36.960	11.275	1.300	26.985
27.005	(4)	132	10.240	10	38.280	36.960	11.275	1.320	27.005
27.015	(5)	133	10.240	10	38.290	36.960	11.275	1.330	27.015
27.025	(6)	134	10.240	10	38.300	36.960	11.275	1.340	27.025
27.035	(7)	135	10.240	10	38.310	36.960	11.275	1.350	27.035
27.055	(8)	137	10.240	10	38.330	36.960	11.275	1.370	27.055
27.065	(9)	138	10.240	10	38.340	36.960	11.275	1.380	27.065
27.075	(10)	139	10.240	10	38.350	36.960	11.275	1.390	27.075
27.085	(11)	140	10.240	10	38.360	36.960	11.275	1.400	27.085
27.105	(12)	142	10.240	10	38.380	36.960	11.275	1.420	27.105
27.115	(13)	143	10.240	10	38.390	36.960	11.275	1.430	27.115
27.125	(14)	144	10.240	10	38.400	36.960	11.275	1.440	27.125
27.135	(15)	145	10.240	10	38.410	36.960	11.275	1.450	27.135
27.155	(16)	147	10.240	10	38.430	36.960	11.275	1.470	27.155
27.165	(17)	148	10.240	10	38.440	36.960	11.275	1.480	27.165
27.175	(18)	149	10.240	10	38.450	36.960	11.275	1.490	27.175
27.185	(19)	150	10.240	10	38.460	36.960	11.275	1.500	27.185
27.205	(20)	152	10.240	10	38.480	36.960	11.275	1.520	27.205
27.215	(21)	153	10.240	10	38.490	36.960	11.275	1.530	27.215
27.225	(22)	154	10.240	10	38.500	36.960	11.275	1.540	27.225
27.235	(24)	155	10.240	10	38.510	36.960	11.275	1.550	27.235
27.245	(25)	156	10.240	10	38.520	36.960	11.275	1.560	27.245
27.255	(23)	157	10.240	10	38.530	36.960	11.275	1.570	27.255
27.265	(26)	158	10.240	10	38.540	36.960	11.275	1.580	27.265
27.275	(27)	159	10.240	10	38.550	36.960	11.275	1.590	27.275
27.285	(28)	160	10.240	10	38.560	36.960	11.275	1.600	27.285
27.295	(29)	161	10.240	10	38.570	36.960	11.275	1.610	27.295
27.305	(30)	162	10.240	10	38.580	36.960	11.275	1.620	27.305
27.315	(31)	163	10.240	10	38.590	36.960	11.275	1.630	27.315
27.325	(32)	164	10.240	10	38.600	36.960	11.275	1.640	27.325
27.335	(33)	165	10.240	10	38.610	36.960	11.275	1.650	27.335
27.345	(34)	166	10.240	10	38.620	36.960	11.275	1.660	27.345
27.355	(35)	167	10.240	10	38.630	36.960	11.275	1.670	27.355
27.365	(36)	168	10.240	10	38.640	36.960	11.275	1.680	27.365
27.375	(37)	169	10.240	10	38.650	36.960	11.275	1.690	27.375
27.385	(38)	170	10.240	10	38.660	36.960	11.275	1.700	27.385
27.395	(39)	171	10.240	10	38.670	36.960	11.275	1.710	27.395
27.405	(40)	172	10.240	10	38.680	36.960	11.275	1.720	27.405

**CRYSTAL**

- X 1 crystal 10.240MHz =  $f_1$
- X 2 crystal 36.960MHz =  $f_4$
- X 3 crystal 11.275MHz =  $f_5$

**Table 1 FREQUENCY OF SYNTHESIS CHART**

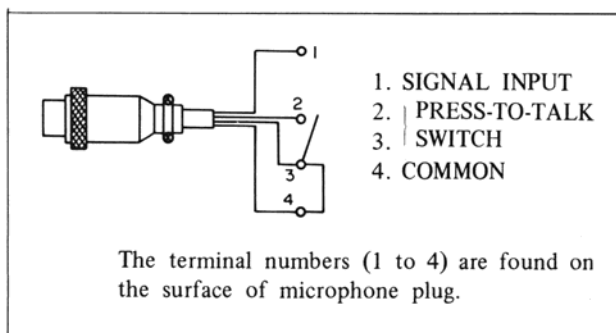
## ALIGNMENT

### EQUIPMENT REQUIRED

Frequency Counter:	0 to 40MHz (High Sensitivity)	DC V.T.V.M.:	0 to 10V
Synchroscope:	0 to 50MHz	DC Milliammeter:	0 to 500mA with Low-pass Filter
Signal Generator:	10MHz to 30MHz with 1000Hz AM mod.	Dummy Load 8 ohms and 50 ohms:	Non-inductive
Audio Signal Generator:	1000Hz (sine wave)	Spectrum Analyzer or Field Strength Meter	
Audio Attenuator:	0 to 100dB	CM Coupler	
RF Output Power Meter:	0 to 5W at 27MHz	DC Power Supply:	13.8V, 2A
RF Voltmeter:	0 to 3V, 0 to 50MHz		
AC V.T.V.M.:	0 to 10V		

### [NOTE]

- 1- Keep supply voltage to 13.8V always during the alignment.
- 2- The tools to be used for the alignment should be non-metallic ones.
- 3- Be sure to keep 50 ohms dummy load connectable with the antenna terminal all the way during the transmitter alignment.
- 4- As to the alignment of the modulation circuit, be sure to use the microphone plug shown in Figure 3 to be connected to it.



**Figure 3 CONNECTION OF MICROPHONE PLUG**

### PHASE LOCKED LOOP (P.L.L.) CIRCUIT ALIGNMENT

STEP	CONNECTION OF MEASURING INSTRUMENT	ADJUSTMENT	PROCEDURE
1 (10.240MHz)	Connect a frequency counter, through 5pF capacitor, to the test point [201] (the terminal No. 1 of IC-201).	C202	Adjust so that the frequency counter reads within 10.240MHz $\pm$ 300Hz. (The oscillation voltage then is about 0.4 to 0.5V . . . . . the value on RF V.T.V.M.)
2 (36.960MHz)	1) Connect an RF voltmeter to the test point [211] (the base of Q205). 2) Make the secondary of V.C.O. coil T201 be short-circuited.	T203	1) Adjust so that the RF voltmeter reads the maximum. 2) Next, rotate the core of T203 counterclockwise so that such maximum reading of the voltmeter is lowered by 0.5dB. (The oscillation voltage then is about 0.7 to 1.2V.)
3 (36.960MHz)	1) Connect the frequency counter, through 5pF capacitor, to the test point [211] (the base of Q205). 2) Make the secondary of V.C.O. coil T201 be short-circuited.	C222	Adjust so that the frequency counter reads within 36.960MHz $\pm$ 300Hz.
4 (V.C.O.)	1) Connect a DC V.T.V.M. to the test point [205] (the terminal No. 1 of IC-202). 2) Connect the frequency counter, through 5pF capacitor, to the test point [206] (the secondary of T201).	T201	1) Set the channel selector to "19" channel. 2) Adjust so that the DC V.T.V.M. reads exactly 3.0V. 3) Make sure the frequency counter is reading 38.460MHz. (The oscillation voltage at the test point [206] is about 0.7 to 1.2V.) 4) Set in turn the channel selector to "1" channel and/or "40" channel and make sure the DC V.T.V.M. reads within 2.0V to 4.0V.
5 (P.L.L. Lock)	Connect the DC V.T.V.M. to the test point [204] (the terminal No. 4 of IC-202).		Make sure the DC V.T.V.M. is reading about 6.4V. If it otherwise reads "0" this means the P.L.L. has not been locked.
6 (38MHz Filter)	Connect a synchroscope to the test point [208] (the output terminal of the filter coil T202).	T202	1) Set the channel selector to "19" channel. 2) Adjust so that the maximum waveform (amplitude) appears on the synchroscope. 3) Set in turn the channel selector to "1" channel and/or "40" channel to make sure the waveform doesn't decrease in size.  Note: Generally speaking, the T202 requires no alignment even at the time of parts replacement since it has been factory-delivered only after undergoing the alignment.

## RECEIVER ALIGNMENT

STEP	CONNECTION OF MEASURING INSTRUMENT	ADJUSTMENT	PROCEDURE
1 (11.730MHz)	Connect the frequency counter, through 5pF capacitor to the test point [3] (the secondary of T6)	T6	Adjust so that the frequency counter reads within 11.730MHz $\pm$ 300Hz. (The oscillation voltage then is about 0.5 to 0.9V.)
2 (IF)	1) Connect an AC V.T.V.M. to both sides of the speaker voice coil lug. 2) Connect a signal generator, through 0.01MFD capacitor, to the test point [1] (the base of Q2). 3) Set the signal generator to 11.275MHz, modulation 1000H, 30%. Note: Be sure to connect the ground wire of signal generator to the ground of the external antenna socket.	T5 T4 T3	Adjust so that the AC V.T.V.M. reads the maximum.
3 (RF)	1) Connect the AC V.T.V.M. to both sides of the speaker voice coil lug. 2) Connect the signal generator to the external antenna socket. 3) Set the signal generator to 27.185MHz (19 channel), modulation 1000Hz, 30%.	T2 T1	1) Set the channel selector to 19 channel. 2) Adjust the AC V.T.V.M. until it reads the maximum.

## TRANSMITTER AND MODULATOR ALIGNMENT

- 1- When the set is made ready for the transmitting operation, be sure to always connect the RF output power meter and 50 ohms dummy load to the external antenna socket—this should never be forgotten even if it is not noted down specifically. If otherwise, the final transistor Q305 may be damaged.
- 2- When making the connection of measuring instruments, see Figure 4.

STEP	CONNECTION OF MEASURING INSTRUMENT	ADJUSTMENT	PROCEDURE
1 (11.275MHz)	1) Connect the frequency counter, through 5pF capacitor, to the test point [302] (the drain of Q301). 2) Make the secondary of V.C.O. coil T201 be short-circuited.	T301	Adjust so that the frequency counter reads within 11.275MHz $\pm$ 300Hz. (The oscillation voltage then is about 0.8 to 1.8V.)
2 (27MHz Filter)	Connect the synchroscope to the test point [304] (the secondary of T302).	T302	1) Set the channel selector to "19" channel. 2) Adjust so that the maximum waveform (amplitude) appears on the synchroscope. 3) Set in turn the channel selector to "1" channel and/or "40" channel to make sure the waveform doesn't decrease in size. Note: Generally speaking, the T302 requires no alignment even at the time of parts replacement since it has been factory-delivered only after undergoing the adjustment.
3 (Driver)	1) Remove the plug which have been inserted in the test points [309] and [310] of the set. 2) Connect in turn DC milliammeter, through the RF rejection filter shown in Figure 5, to the test points [309] and [310].	T303	Adjust so that the DC milliammeter connected to the test point [310] reads the maximum (driver current).
4 (Driver)	Same as the above	T304	Adjust so that the DC milliammeter connected to the test point [310] indicates the dip point. The amperage then is about 60 to 90 mA.
5 (Final)	Same as the step 3	L302	Adjust so that the DC milliammeter connected to the test point [309] reads 450 $\pm$ 30 mA (final current).
6 ( $\pi$ -Filter)	Connect the RF output power meter and 50 ohms dummy load to the antenna socket.	L303	Adjust so that the RF output power meter reads the maximum. The reading then should not exceed 4W. (FCC Rules and Regulations Part 95, Section 95. 43.)
7	Repeat the steps 3 to 6 until the best results will be obtained.		
8 (Second harmonic)	Connect the RF output power meter, 50 ohms dummy load and spectrum analyzer (or field strength meter), through CM coupler, to the antenna socket.	L305	1) Set the channel selector to "19" channel. 2) Adjust so that 54MHz output component (second harmonic) becomes the minimum on the spectrum analyzer (or field strength meter).
9 (Third harmonic)	Same as the above.	L304	1) Set the channel selector to "19" channel. 2) Adjust so that 81MHz output component (third harmonic) becomes the minimum on the spectrum analyzer (or field strength meter).



STEP	CONNECTION OF MEASURING INSTRUMENT	ADJUSTMENT	PROCEDURE
10 (Modulation)	1) Connect the RF output power meter, 50 ohms dummy load and synchroscope, through CM coupler, to the antenna socket. 2) Connect audio signal generator, attenuator and AC V.T.V.M. to the microphone socket (using the microphone plug shown in Figure 3). 3) Keep the output of signal generator to 1000Hz, 1V.	R112	1) Turn R112 clockwise until the modulation limiter circuit stops its function. 2) Make the attenuator operate so that the modulation factor of RF output waveform appeared on the synchroscope becomes 50%. Then adjust the attenuator so that the microphone input signal increases 20dB over the original one. (The microphone input then is about 30mV.) 3) Adjust R112 so that the modulation factor of RF output waveform appeared on the synchroscope becomes about 80%. (See Figure 6.)

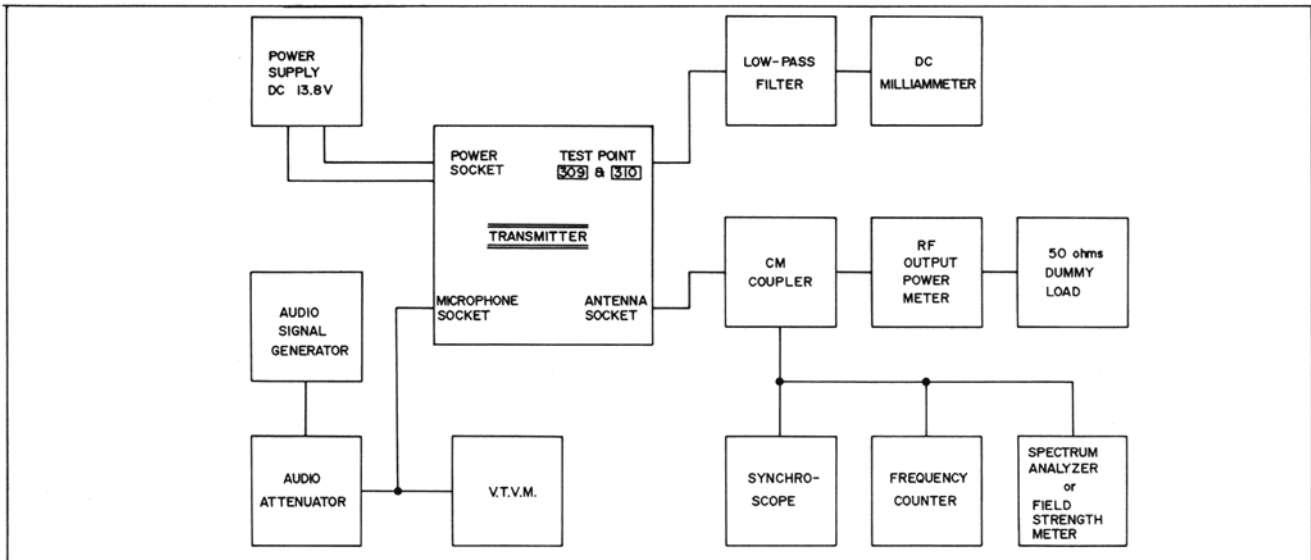


Figure 4

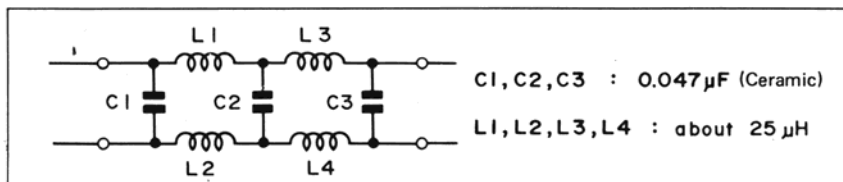


Figure 5 RF REJECTION FILTER  
(LOW-PASS FILTER)

**SIGNAL/RF POWER METER ADJUSTMENT**

STEP	CONNECTION OF MEASURING INSTRUMENT	ADJUSTMENT	PROCEDURE
1	Connect the signal generator to the antenna socket and set the frequency to 27.185MHz ("19" channel) and the modulation to 1000Hz, 30%. Keep the output of signal generator to 40dB.	R19	1) Set the channel selector to "19" channel. 2) Adjust so that the signal/RF power meter indicates "9" on the "SIGNAL" scale.
2	Connect the RF output power meter and 50 ohms dummy load to the antenna socket.	R319	1) Set the channel selector to "19" channel and make the set be ready for the transmitting operation (non-modulation however). 2) First make sure of what value the pointer of signal/RF power meter indicates on the "POWER" scale and then adjust R319 so that such a value becomes nearly the same as that of the RF output power meter connected to the antenna socket. (The RF power output then is about 3.5W.)

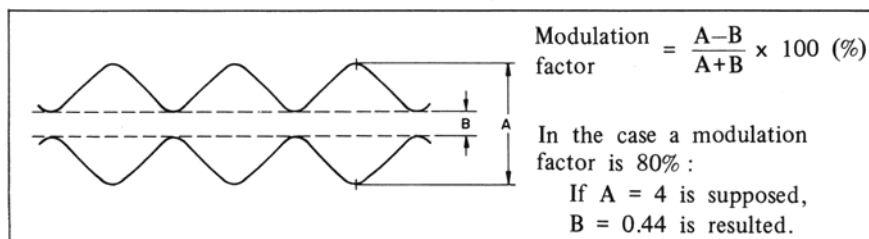


Figure 6

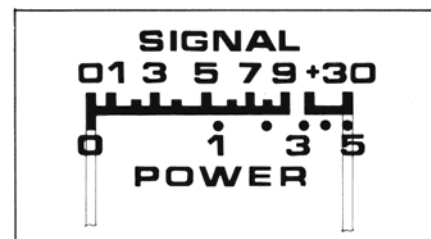
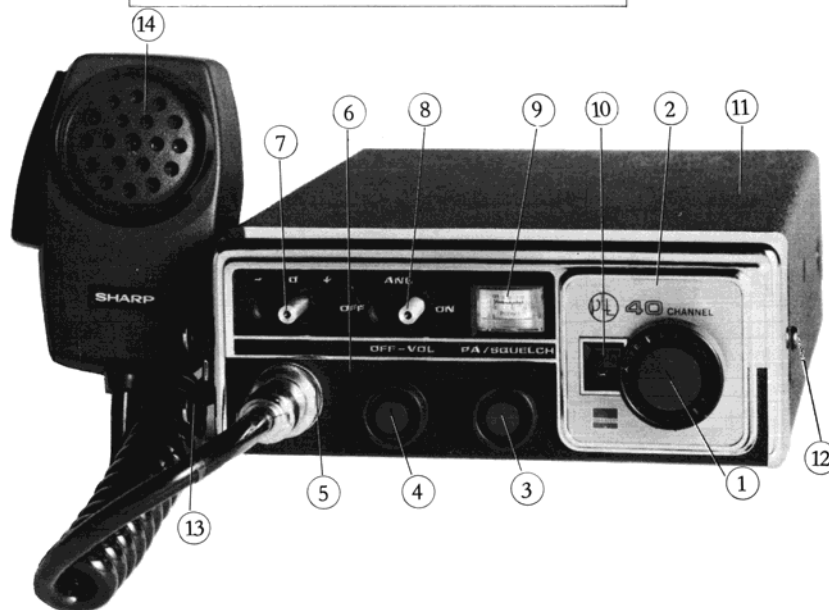


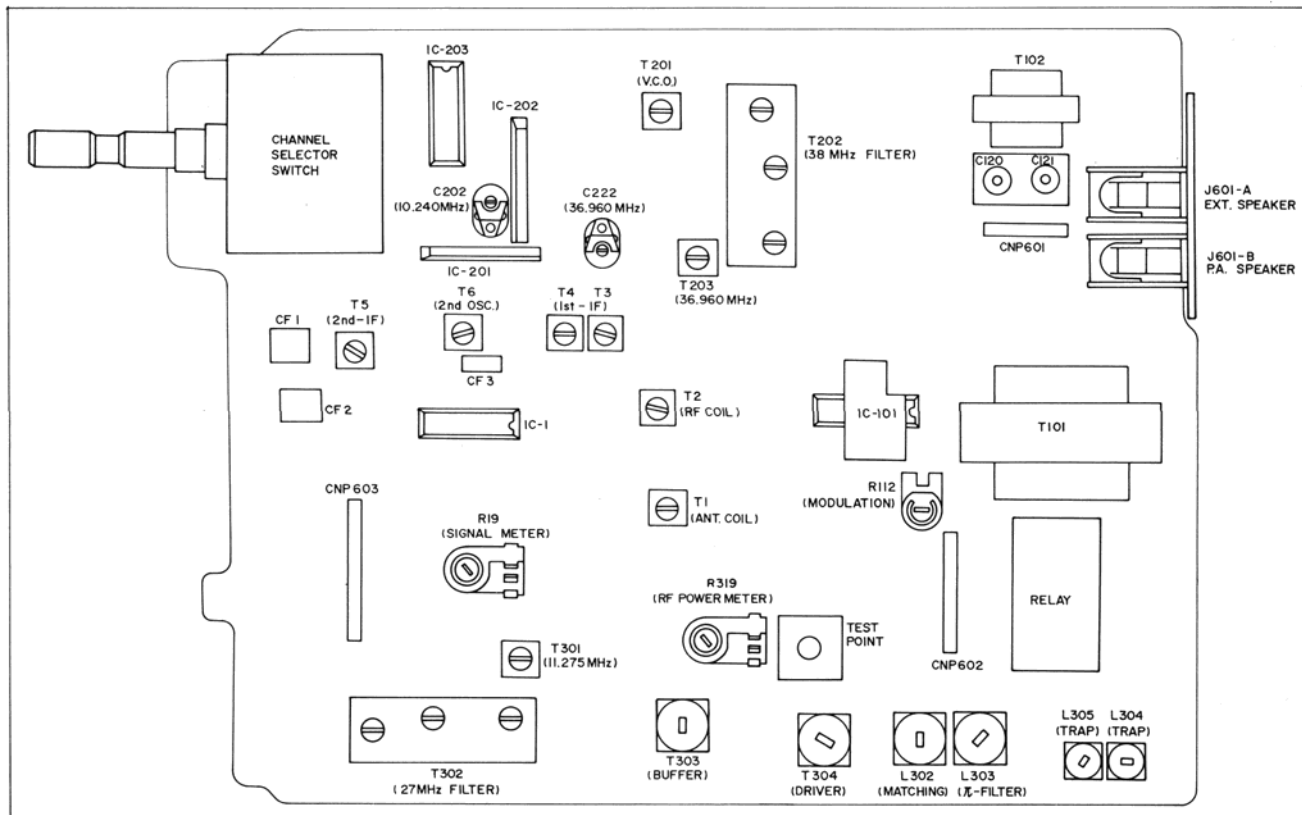
Figure 7 SIGNAL/RF POWER METER (ME601)

## FRONT PARTS LAYOUT



- |   |  |
|---|--|
| ① Channel Selector Knob (JKNBN0097AFSA)         | ⑧ A.N.L. Switch Knob (JKNBM0236AFSA)       |
| ② Indication Metal, 40 Channel (HINDM1121AFSA)  | ⑨ SIGNAL/RF POWER Meter (RMTRE0061AFZZ)    |
| ③ Squelch/P.A. Switch Knob (JKNBN0098AFSA)      | ⑩ Lens, Channel Indication (PLNS-0008AFSA) |
| ④ Off-On/Volume Knob (JKNBN0098AFSA)            | ⑪ Cabinet, Top (GCABB-3031AFSA)            |
| ⑤ Microphone Socket (QSOCZ2465AFZZ)             | ⑫ Cabinet, Bottom (GCABA3432AFSA)          |
| ⑥ Front Panel (GWAKP1064AFSA)                   | ⑬ Microphone Hanger (PHAG-8001AFFC)        |
| ⑦ Delta Fine Tuning Switch Knob (JKNBM0236AFSA) | ⑭ Microphone Assembly (RMICD0206AFZZ)      |

**Figure 8 FRONT PARTS LAYOUT**



**Figure 9 ALIGNMENT POINTS**

## CHANNEL SELECTOR SWITCH

Connection table of channel selector switch (SW4) for each channel.

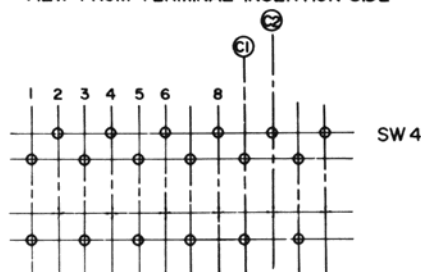
CHANNEL	TERMINAL NO. OF THE SW4-A (11-17)	1	2	3	4	5	6	-	-	8	N
	TERMINAL NO. OF IC-203	1(1)	2(2)	3(4)	4(8)	5(16)	6(32)	7(64)	8(128)	12	
1										●	128
2		○									129
3			○							●	130
4				○						●	132
5		○		○							133
6			○	○							134
7		○	○	○							135
8		○			◎					●	137
9			○		◎					●	138
10		○	○		◎						139
11				○	◎					●	140
12			○	○	◎						142
13		○	○	○	◎						143
14						◎				●	144
15		○				◎					145
16		○	○			◎					147
17				○		◎				●	148
18		○		○		◎				●	149
19			○	○		◎				●	150
20					◎	◎				●	152
21		○			◎	◎				●	153
22			○		◎	◎				●	154
23		○		○	◎	◎				●	155
24		○	○		◎	◎				●	156
25				○	◎	◎				●	157
26			○	○	◎	◎					158
27		○	○	○	◎	◎				●	159
28							◎				160
29		○					◎			●	161
30			○				◎				162
31		○	○				◎			●	163
32				○			◎			●	164
33		○		○			◎			●	165
34			○	○			◎			●	166
35		○	○	○			◎			●	167
36					◎		◎			●	168
37		○			◎		◎			●	169
38			○		◎		◎			●	170
39		○	○		◎		◎			●	171
40				○	◎		◎			●	172

**NOTES:**

1. Terminals marked ○ are connected with the terminal (C1).
2. Terminals marked ◎ are connected with the Terminal (C2).
3. The mark ● given on the terminal No.8 of SW4-A shows that this terminal comes in contact with the COMMON terminal if the set gets in a channel-to-channel situation.
4. The terminal No. 8 of IC203 is always in connection with +B and kept to "HIGH" level.

**QSW-R0139AFZZ**

VIEW FROM TERMINAL INSERTION SIDE



MOUNTING FACE

**Table 2 CHANNEL SELECTOR SWITCH**

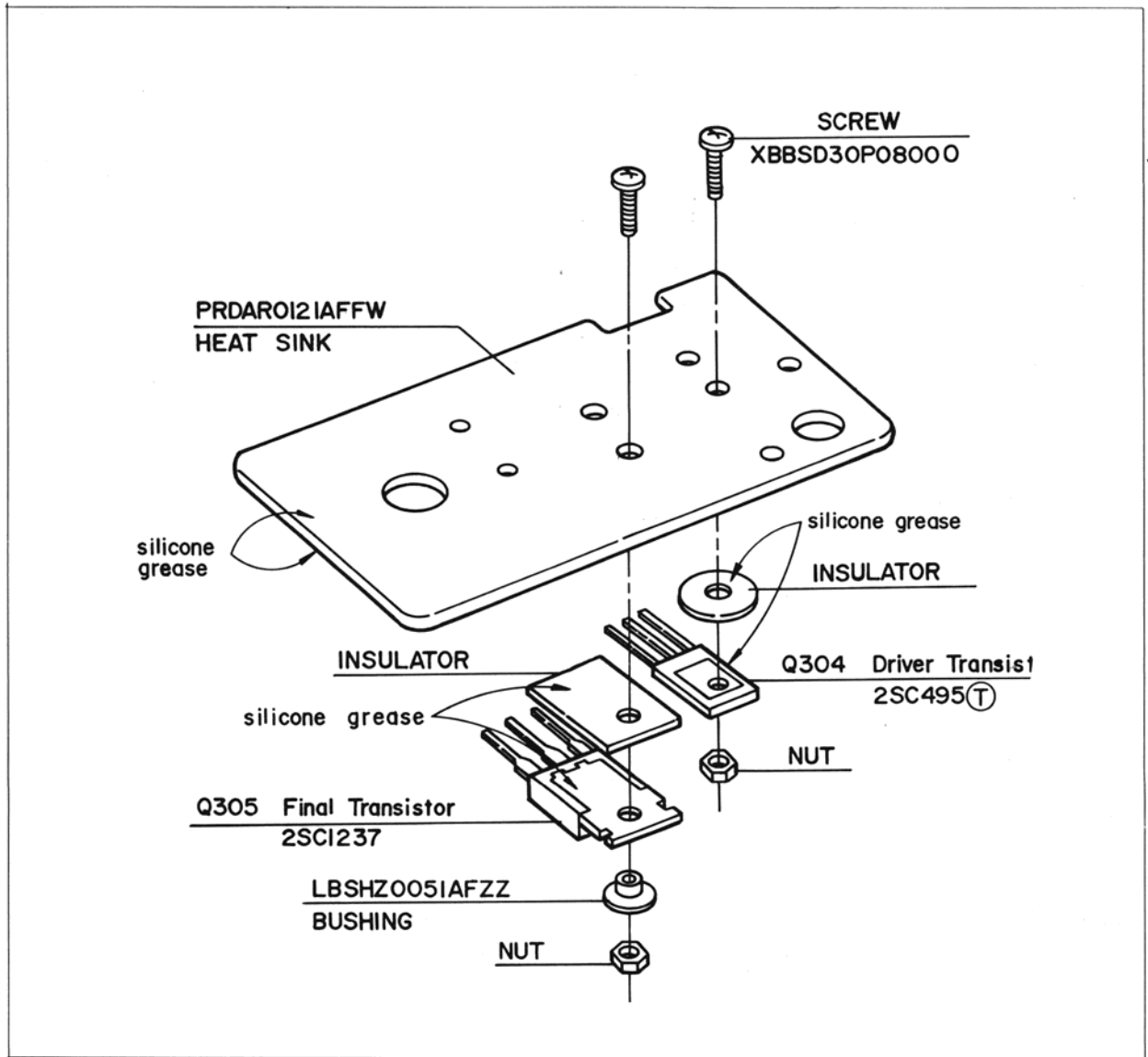


Figure 12 HOW TO SET THE TRANSISTOR Q304 AND Q305

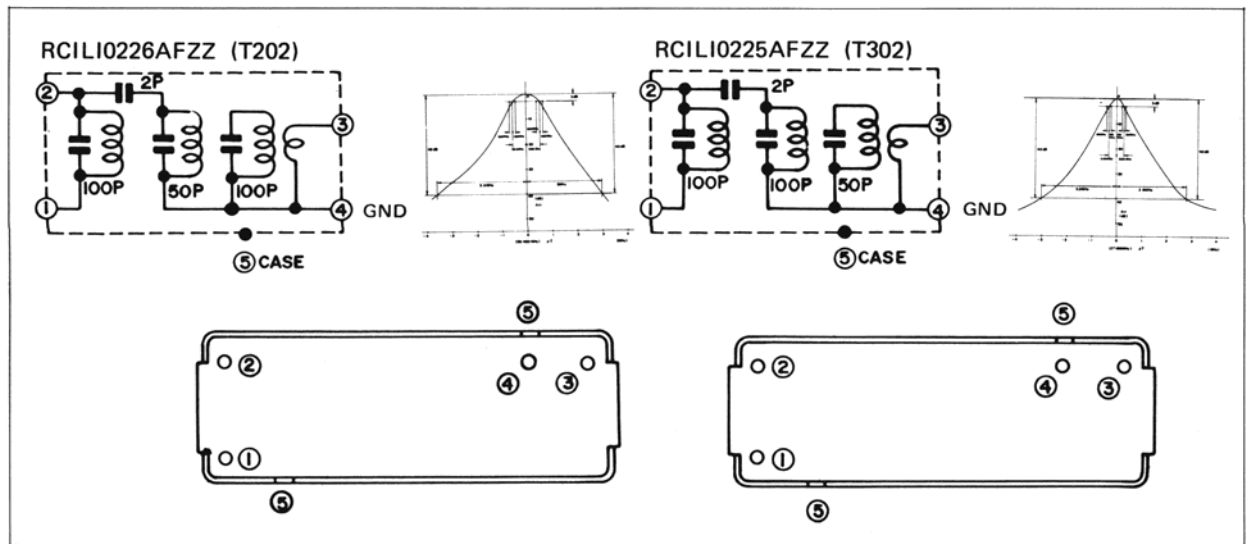


Figure 13 TRANSFORMERS (T202 and T302) BASING

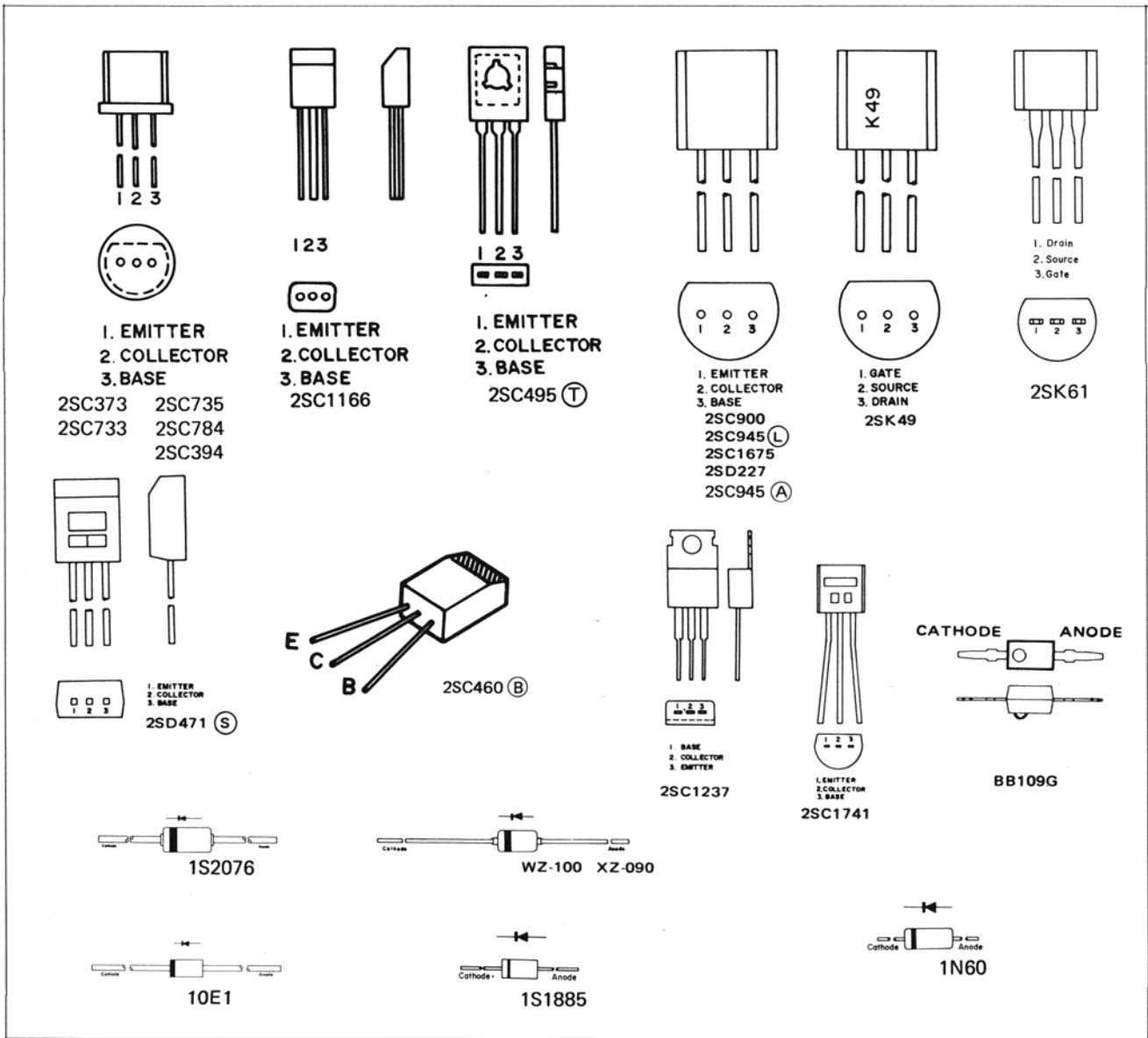


Figure 14 SEMICONDUCTORS BASING

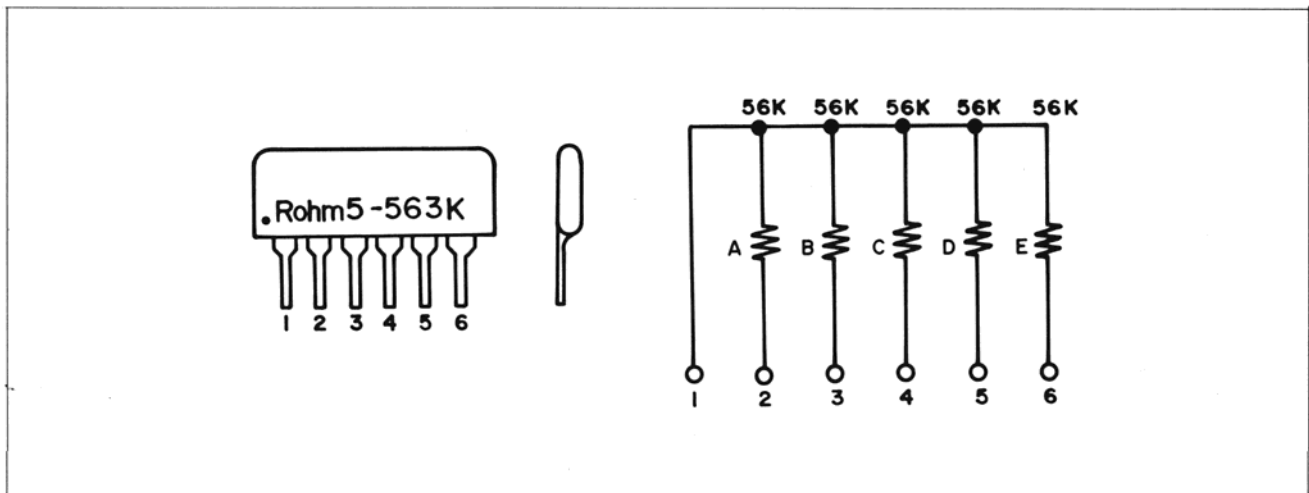


Figure 15 RESISTOR ARRAY

# REPLACEMENT PARTS LIST

## "HOW TO ORDER REPLACEMENT PARTS"

To have your order filled promptly and correctly, please furnish the following informations.

1. MODEL NUMBER
2. REF. NO.
3. PART NO.
4. DESCRIPTION

REF. NO.	PART NO.	DESCRIPTION	PRICE	REF. NO.	PART NO.	DESCRIPTION	PRICE		
<b>INTEGRATED CIRCUITS</b>									
IC1	RH-IX1030AFZZ	2nd-IF Amplifier and Detector		Q304	VS2SC495-T/-1	Transmitter, Driver (2SC495 (T))			
IC101	RH-IX1020AFZZ	Driver and Audio Amplifier		Q305	VS2SC1237-1F	Transmitter, Final (2SC1237)			
IC201	RH-IX1039AFZZ	Oscillator and Divider		<b>DIODES</b>					
IC202	RH-IX1038AFZZ	Phase Comparator and Low Pass Filter		D1	VHD1S2076//-1	Static Protector (1S2076)			
IC203	RH-IX1061AFZZ	Programmable Divider		D2	VHD1S2076//-1	Static Protector (1S2076)			
<b>TRANSISTORS</b>									
Q1	VS2SC1675M/-1 or VS2SC784-R/1F	RF Amplifier (2SC1675 (M) or 2SC784 (R))		D3	VHEWZ-100//1F	Zener Diode, Voltage Regulator (10V ± 0.5V)			
Q2	VS2SC1675M/-1 or VS2SC394-Y/-1	1st-Mixer (2SC1675 (M) or 2SC394 (Y))		D4	VHD1N60////-1	A.N.L. (Automatic Noise Limiter) (1N60)			
Q3	VS2SC945AK/-1 or VS2SC373-G/-1	AGC Amplifier (2SC945 (A)K or 2SC373)		D5	VHD1N60////-1	AGC Detector (1N60)			
Q4	VS2SC945AP/-1 or VS2SC373-G/-1	AGC Amplifier (2SC945 (A)P or 2SC373)		D6	VHD1N60////-1	AGC Detector (1N60)			
Q6	VS2SC460-B/-1 or VS2SC394-Y/-1	2nd-Mixer (2SC460 (B) or 2SC394 (Y))		D7	VHD1S2076//-1	Static Protector (1S2076)			
Q101	VS2SC945LP/-1	AF Amplifier (2SC945 (L)P)		D8	VHD1N60////-1	S (Signal) Meter (1N60)			
Q102	VS2SD227-V/-1 or VS2SC735-Y/-1	Modulation Limiter Amplifier (2SD227 (V) or 2SC735 (Y))		D9	VHD1N60////-1	Overload (1N60)			
Q103	VS2SC945AP/-1 or VS2SC373-G/-1	Modulation Limiter Amplifier (2SC945 (A)P or 2SC373)		D101	VHD1N60////-1	Modulation Detector (1N60)			
Q104	VS2SC900-U/-1 or VS2SC733-B/-1	Squelch Voltage Amplifier (2SC900 (U) or 2SC733 (BL))		D102	VHD1S1885//-1	Circuit Protector (1S1885)			
Q201	VS2SC945AP/-1 or VS2SC373-G/-1	P.L.L. Synthesizer, Gate (2SC945 (A)P or 2SC373)		D103	VHD10E1////-1	Protector (10E1)			
Q202	VS2SC945AP/-1 or VS2SC394-Y/-1	P.L.L. Synthesizer, Buffer (2SC945 (A)P or 2SC394 (Y))		D201	VHEXZ-090//1	Zener Diode, Voltage Regulator (9V ± 0.25V)			
Q203	VS2SK49-F//1 or VS2SK61-Y//1	P.L.L. Synthesizer, FET, V.C.O. (Voltage Controlled Oscillator) (2SK49 (F) or 2SK61 (Y))		D202	VHCB109G//1	Varicap, V.C.O. (BB109G)			
Q204	VS2SC945AP/-1 or VS2SC373-G/-1	P.L.L. Synthesizer, Buffer (2SC945 (A)P or 2SC373)		D301	VHD1S2076//-1	RF Power Meter (1S2076)			
Q205	VS2SC945AP/-1 or VS2SC373-G/-1	P.L.L. Synthesizer, Mixer (2SC945 (A)P or 2SC373)		<b>CRYSTALS</b>					
Q206	VS2SK61-Y//1 or VS2SK49-F//1	P.L.L. Synthesizer, FET, 36.960MHz Oscillator (2SK61 (Y) or 2SK49 (F))		X1	RCRSB0053AFZZ	10.240MHz			
Q207	VS2SD471-S/-1 or VS2SC1741//1	P.L.L. Synthesizer, Voltage Regulator (2SD471 (S) or 2SC1741)		X2	RCRSB0054AFZZ	36.960MHz			
Q301	VS2SK49-F//1 or VS2SK61-Y//1	Transmitter, FET, Crystal (11.275MHz) Oscillator (2SK49 (F) or 2SK61 (Y))		X3	RCRSB0015AFZZ	11.275MHz			
Q302	VS2SC945AP/-1 or VS2SC735-Y/-1	Transmitter, 27MHz Mixer (2SC945 (A)P or 2SC735 (Y))		X4	RCRSB0016AFZZ	11.730MHz			
Q303	VS2SC1166-Y-1 or VS2SC1166-O-1	Transmitter, Buffer Amplifier (2SC1166 (Y) or (O))		<b>CERAMIC FILTERS</b>					
				CF1	RFILA0055AFZZ	455kHz, 2nd-IF			
				CF2	RFILA0050AFZZ	455kHz, 2nd-IF			
				CF3	RFILA0001AFZZ	455kHz			
				<b>COILS</b>					
				L1	RCILZ0014AGZZ	2nd-IF, 1mH			
				L101	RCILC0023AFZZ	AF Choke			
				L102, L103, L104	RCILC0011AFZZ	RF Choke			
				L201			RCILC0024AFZZ	Low Pass Filter	
				L301			RCILC0011AFZZ	RF Choke	
				L302			RCILR0135AFZZ	Transmitter, Matching (Loading)	
				L303	RCILR0055AFZZ	Transmitter, π-Filter			
				L304	RCILC0055AFZZ	Trap, 81MHz			
				L305	RCILC0055AFZZ	Trap, 54MHz			
				L306	RCILR0310AFZZ	Choke			
				L601, L602	RCILC0059AFZZ	Power Choke			

# PARTS LIST

REF. NO.	PART NO.	DESCRIPTION	PRICE	REF. NO.	PART NO.	DESCRIPTION	PRICE
<b>TRANSFORMERS</b>							
T1	RCILA0377AFZZ	Antenna		C125	VCQYKU1HM222M	.0022MFD, 50V, ±20%, Mylar	
T2	RCILR0304AFZZ	RF		C126	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar	
T3	RCILIO210AFZZ	1st-IF (11.275MHz)		C127	VCKZPU1HF103Z	.01MFD	
T4	RCILIO210AFZZ	1st-IF (11.275MHz)		C131	VCKZPU1HF222Z	.0022MFD	
T5	RCILIO154AFZZ	2nd-IF (455kHz)		C201	VCCCPU1HH150J	15PF(CH), 50V, ±5%, Ceramic	
T6	RCILIO210AFZZ	2nd Local Oscillator (11.730MHz)		C202	RTO-H1009AFZZ	Trimmer Capacitor, 10.240MHz Oscillator	
T101	RTRNM0050AFZZ	Output and Modulation		C203	VCCCPU1HH100F	10PF(CH), 50V, ±1PF, Ceramic	
T102	RTRNC0003AFZZ	Power Choke		C205	VCKZPU1HF103Z	.01MFD	
T201	RCILB0383AFZZ	Voltage Controlled Oscillator		C206	VCKZPU1HF103Z	.01MFD	
T202	RCILIO226AFZZ	38MHz Filter		C207	VCKZPU1HF103Z	.01MFD	
T203	RCILB0383AFZZ	36.960MHz Oscillator		C208	VCCCPU1HH180J	18PF(CH), 50V, ±5%, Ceramic	
T301	RCILB0378AFZZ	Transmitter, 11.275MHz Oscillator		C209	VCCCPU1HH100F	10PF(CH), 50V, ±1PF, Ceramic	
T302	RCILIO225AFZZ	Transmitter, 27MHz Filter		C210	VCKZPU1HF103Z	.01MFD	
T303	RCILB0221AFZZ	Transmitter, Buffer		C211	VCCUPU1HJ150J	15PF (UJ), 50V, ±5%, Ceramic	
T304	RCILR0037AFZZ	Transmitter, Driver		C213	VCCCPU1HH220J	22PF (CH), 50V, ±5%, Ceramic	
<b>CAPACITORS</b>							
(Unless otherwise specified capacitors are 50V, +80 -20%, Ceramic Type)							
C1	VCKZPU1HF103Z	.01MFD		C214	VCCSPU1HL680J	68PF, 50V, ±5%, Ceramic	
C2	VCKZPU1HF103Z	.01MFD		C215	VCQYKU1HM223M	.022MFD, 50V, ±20%, Mylar	
C3	VCKZPU1HF103Z	.01MFD		C216	VCCSPU1HL121J	120PF, 50V, ±5%, Ceramic	
C4	VCKZPU1HF103Z	.01MFD		C218	VCCCPU1HH5R0C	5PF (CH), 50V, ±0.25PF, Ceramic	
C5	VCKYPU1HB223M	.022MFD, 50V, ±20%, Ceramic		C219	VCKZPU1HF103Z	.01MFD	
C6	VCKYPU1HB472M	.0047MFD, 50V, ±20%, Ceramic		C220	VCCCPU1HH5R0C	5PF (CH), 50V, ±0.25PF, Ceramic	
C7	VCKZPU1HF223Z	.022MFD		C222	RTO-H1009AFZZ	Trimmer Capacitor, 36.960MHz Oscillator	
C8	VCKZPU1HF103Z	.01MFD		C223	VCKZPU1HF103Z	.01MFD	
C9	VCCSPU1HL2R0C	2PF, 50V, ±0.25PF, Ceramic		C224	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80 -20%, Ceramic	
C10	VCCSPU1HL150J	15PF, 50V, ±5%, Ceramic		C226	VCCSPU1HL390J	39PF, 50V, ±5%, Ceramic	
C11	VCKYPU1HB472M	.0047MFD, 50V, ±20%, Ceramic		C227	VCKYPU1HB102M	.001MFD, 50V, ±20%, Ceramic	
C12	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic		C228	VCCCPU1HH5R0C	5PF (CH), 50V, ±0.25PF, Ceramic	
C13	VCKZPU1HF103Z	.01MFD		C230	VCCUPU1HJ120J	12PF (UJ), 50V, ±5%, Ceramic	
C15	VCKZPU1HF103Z	.01MFD		C231	VCKZPU1HF103Z	.01MFD	
C16	VCKZPU1HF103Z	.01MFD		C232	VCKZPU1HF103Z	.01MFD	
C17	VCQYKU1HM103M	.01MFD, 50V, ±20%, Mylar		C233	VCCSPU1HL680J	68PF, 50V, ±5%, Ceramic	
C20	VCQYKU1HM223M	.022MFD, 50V, ±20%, Mylar		C301	VCCSPU1HL560J	56PF, 50V, ±5%, Ceramic	
C22	VCCSPU1HL121J	120PF, 50V, ±5%, Ceramic		C302	VCKZPU1HF103Z	.01MFD	
C23	VCKYPU1HB102M	.001MFD, 50V, ±20%, Ceramic		C304	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic	
C24	VCCSPU1HL820J	82PF, 50V, ±5%, Ceramic		C305	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic	
C25	VCKZPU1HF103Z	.01MFD		C308	VCKZPU1HF103Z	.01MFD	
C26	VCKZPU1HF103Z	.01MFD		C309	VCKZPU1HF103Z	.01MFD	
C30	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80 -20%, Ceramic		C310	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic	
C31	VCCSPU1HL470J	47PF, 50V, ±5%, Ceramic		C311	VCCSPU1HL180J	18PF, 50V, ±5%, Ceramic	
C32	VCKZPU1HF103Z	.01MFD		C312	VCKZPU1HF103Z	.01MFD	
C33	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar		C313	VCKZPU1HF103Z	.01MFD	
C35	VCCSPU1HL120J	12PF, 50V, ±5%, Ceramic		C314	VCCSPU1HL221J	220PF, 50V, ±5%, Ceramic	
C36	VCCSPU1HL470J	47PF, 50V, ±5%, Ceramic		C315	VCCSPU1HL471J	470PF, 50V, ±5%, Ceramic	
C37	VCCSPU1HL681J	680PF, 50V, ±5%, Ceramic		C316	VCCSPU1HL220J	22PF, 50V, ±5%, Ceramic	
C101	VCQYKU1HM333M	.033MFD, 50V, ±20%, Mylar		C317	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80 -20%, Ceramic	
C102	VCQYKU1HM102M	.001MFD, 50V, ±20%, Mylar		C318	VCCSPU1HL511J	510PF, 50V, ±5%, Ceramic	
C104	VCQYKU1HM153M	.015MFD, 50V, ±20%, Mylar		C319	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80 -20%, Ceramic	
C106	VCQYKU1HM223M	.022MFD, 50V, ±20%, Mylar		C320	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80 -20%, Ceramic	
C107	VCQYKU1HM222M	.0022MFD, 50V, ±20%, Mylar		C321	VCCSPU1HL181J	180PF, 50V, ±5%, Ceramic	
C108	VCQYKU1HM103M	.01MFD, 50V, ±20%, Mylar					
C109	VCQYKU1HM102M	.001MFD, 50V, ±20%, Mylar					
C119	VCKZPU1HF104Z	.1MFD					
C122	VCKZPU1HF104Z	.1MFD					
C123	VCKZPU1HF103Z	.01MFD					
C124	VCKZPU1HF103Z	.01MFD					

# PARTS LIST

REF. NO.	PART NO.	DESCRIPTION	PRICE	REF. NO.	PART NO.	DESCRIPTION	PRICE
C322	VCCSPU1HL680J	68PF, 50V, ±5%, Ceramic		C323	VCAAKU0XA474M	.47MFD, 6.3V, ±20%, Aluminum	
C324	VCCSPU1HL220J	22PF, 50V, ±5%, Ceramic		C413	VCEAAU1CW106Y	10MFD, 16V, +50 -10%	
C325	VCCSPU1HL560J	56PF, 50V, ±5%, Ceramic		<b>RESISTORS</b>			
C326	VCKZPU1HF103Z	.01MFD		(Unless otherwise specified resistors are 1/4W, ±5%, Carbon Type.)			
C327	VCCSPU1HL101J	100PF, 50V, ±5%, Ceramic		R1	VRD-SU2EY562J	5.6K ohm	
C328	VCCSPU1HL4R0C	4PF, 50V, ±0.25PF, Ceramic		R2	VRD-SU2EY152J	1.5K ohm	
C329	VCCSPU1HL560J	56PF, 50V, ±5%, Ceramic		R3	VRD-ST2EE102J	1K ohm	
C330	VCCSPU1HL220J	22PF, 50V, ±5%, Ceramic		R5	VRD-SU2EY333J	33K ohm	
C402	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80 -20%, Ceramic		R6	VRD-SU2EY472J	4.7K ohm	
C404	VCKZPU1HF103Z	.01MFD		R7	VRD-SU2EY102J	1K ohm	
C405	VCKZPU1HF103Z	.01MFD		R9	VRD-SU2EY153J	15K ohm	
C406	VCKZPU1HF223Z	.02MFD		R10	VRD-SU2BY102J	1K ohm, 1/8W, ±5%, Carbon	
C407	VCKZPU1HF103Z	.01MFD		R12	VRD-ST2EE151J	150 ohm	
C408	VCKZPU1HF103Z	.01MFD		R13	VRD-SU2EY472J	4.7K ohm	
C409	VCKZPU1HF103Z	.01MFD		R14	VRD-SU2EY223J	22K ohm	
C410	VCKYPU1HB103M	.01MFD, 50V, ±20%, Ceramic		R15	VRD-SU2EY153J	15K ohm	
C411	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80 -20%, Ceramic		R16	VRD-SU2EY223J	22K ohm	
C412	VCKYPU1SD103Z	.01MFD (Z5T), 30V, +80 -20%, Ceramic		R17	VRD-SU2EY124J	120K ohm	
C414	VCKZPU1HF103Z	.01MFD		R19	RVR-M0119AFZZ	5K (B) ohm, Pot., S (Signal) Meter Adjust	
C420	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic		R20	VRD-ST2EE224J	220K ohm	
C421	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic		R21	VRD-ST2EE223J	22K ohm	
C422	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic		R22	VRD-ST2EE272J	2.7K ohm	
C423	VCCSPU1HL151J	150PF, 50V, ±5%, Ceramic		R23	VRD-ST2EE471J	470 ohm	
C424	VCKZPU1HF103Z	.01MFD		R24	VRD-ST2EE224J	220K ohm	
C601	VCKZPU1HF103Z	.01MFD		R25	VRD-ST2EE333J	33K ohm	
C602	VCCSPU1HL221J	220PF, 50V, ±5%, Ceramic		R29	VRD-ST2EE103J	10K ohm	
C603	VCCSPU1HL221J	220PF, 50V, ±5%, Ceramic		R30	VRD-ST2EE472J	4.7K ohm	
C604	VCKZPU1HF103Z	.01MFD		R31	VRD-ST2EE473J	47K ohm	
C701	VCCSPU1HL820J	82PF, 50V, ±5%, Ceramic		R32	VRD-SU2EY103J	10K ohm	
C702	VCCSPU1HL330J	33PF, 50V, ±5%, Ceramic		R35	VRD-SU2EY333J	33K ohm	
C703	VCQYKU1HM153M	.015MFD, 50V, ±20%, Mylar		R36	VRD-SU2EY472J	4.7K ohm	
<b>ELECTROLYTIC CAPACITORS</b>				R37	VRD-SU2EY222J	2.2K ohm	
C14	VCEAAU1AW107Y	100MFD, 10V, +50 -10%		R38	VRD-ST2EE122J	1.2K ohm	
C18	VCEAAU1CW106Y	10MFD, 16V, +50 -10%		R39	VRD-ST2EE473J	47K ohm	
C27	VCEAAU1HW105A	1MFD, 50V, +75 -10%		R40	VRD-SU2EY100J	10 ohm	
C28	VCEAAU1EW475A	4.7MFD, 25V, +75 -10%		R41	VRD-ST2EY683J	68K ohm	
C29	VCEAAU1CW106Y	10MFD, 16V, +50 -10%		R102	VRD-ST2EE331J	330 ohm	
C34	VCEAAU1CW106Y	10MFD, 16V, +50 -10%		R103	VRD-ST2EY104J	100K ohm	
C103	VCEAAU1EW475A	4.7MFD, 25V, +75 -10%		R104	VRD-ST2EE154J	150K ohm	
C105	VCEAAU1CW106Y	10MFD, 16V, +50 -10%		R105	VRD-ST2EE153J	15K ohm	
C110	VCEAAU1CW476Y	47MFD, 16V, +50 -10%		R106	VRD-ST2EE102J	1K ohm	
C111	VCEAAU1AW336Y	33MFD, 10V, +50 -10%		R107	VRD-ST2EE103J	10K ohm	
C112	VCSATU1VF104M	.1MFD, 35V, ±20%, Tantalum		R108	VRD-ST2EE331J	330 ohm	
C113	VCEAAU1CW476Y	47MFD, 16V, +50 -10%		R109	VRD-ST2EE222J	2.2K ohm	
C114	VCSATU1VF104M	.1MFD, 35V, ±20%, Tantalum		R110	VRD-ST2EE333J	33K ohm	
C115	VCEAAU1AW477Y	470MFD, 10V, +50 -10%		R111	VRD-ST2EE2R2J	2.2 ohm	
C116	VCEAAU1CW108Y	1000MFD, 16V, +50 -10%		R112	RVR-M0116AFZZ	1K (B) ohm, Modulation Level Adjust	
C117	VCEAAU1EW475A	4.7MFD, 25V, +75 -10%		R113	VRD-ST2EE103J	10K ohm	
C118	VCEAAU1CW106Y	10MFD, 16V, +50 -10%		R114	VRD-SU2EY222J	2.2K ohm	
C128	VCEAAU1CW106Y	10MFD, 16V, +50 -10%		R115	VRD-ST2EE220J	22 ohm	
C204	VCEAAU1HW105A	1MFD, 50V, +75 -10%		R116	VRD-ST2EY332J	3.3K ohm	
C212	VCAAAU1AB105M	.1MFD, 10V, ±20%, Aluminum		R117	VRS-PT3DB2R2K	2.2 ohm, 2W, ±10%, Oxide Film	
C217	VCEAAU1CW106Y	10MFD, 16V, +50 -10%		R202	VRD-ST2EY563 J	56K ohm	
C225	VCEAAU1AW107Y	100MFD, 10V, +50 -10%		R203	VRD-SU2EY103J	10K ohm	
C229	VCEAAU1AW108Y	1000MFD, 10V, +50 -10%		R204	VRD-SU2EY222J	2.2K ohm	
				R205	VRD-ST2EY224J	220K ohm	
				R206	VRD-SU2EY151J	150 ohm	
				R207	VRD-SU2EY471J	470 ohm	





# PARTS LIST

REF. NO.	PART NO.	DESCRIPTION	PRICE
SW1/ R702	RVR-D0104AFZZ	5K (D) ohm, Off-On/Volume Control	
SW2-A, B/R701	RVR-B0137AFZZ	10K (B) ohm, Squelch/P.A. Switch	
SW3	QSW-B0028AGZZ	Switch, A.N.L.	
SW4	QSW-R0139AFZZ	Switch, Channel Selector	
SW5	QSW-B0003AFZZ	Switch, Delta Fine Tuning	
SW6-A ~D/ RY101	RRLYZ0007AFZZ	Relay with Receiver /Transmitter Switch	
C120, C121	RC-KZ1009AFZZ	Feed Through Capacitors with Bracket	
	RCORF0050AFZZ	Ferrite Core	
PL1	RLMPM0058AFZZ	Lamp, Illumination (14V, 80mA)	
	RMICD0206AFZZ	Microphone Assembly (with Press-to-talk Switch)	
ME601	RMTRE0061AFZZ	Meter, Signal/RF Power	
SP601	VSP0080P-208A	Speaker, 8 ohms	
	XBBSC30W08000	Screw (3φ × 8mm), Plus and Minus	
	XNESD50-40000	Nut (5φ)	
	XWHSD30-05000	Washer (3φ)	
	XWHSD50-05000	Washer (5φ)	
	XWSSJ50-13000	Spring Washer (5φ)	